

**Amendments to the Specification:**

Please replace the paragraph bridging pages 37 and 38 with the following amended paragraph:

The above TFTs 201 to 203 are appropriately combined to form a shift ~~resister~~ register circuit, a buffer circuit, a level shifter circuit, a latch circuit and the like, thereby forming the driver circuit 206. For example, in the case where a CMOS circuit is formed, the n-channel TFT 201 and the p-channel TFT 202 may be complementarily connected to each other.